DSG-NPS R&D Meeting Minutes

Date: April 19, 2022

Time: 02:00PM - 03:15PM

<u>Attendees</u>: Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Pablo Campero, Brian Eng, George Jacobs, Mindy Leffel, Tyler Lemon, Marc McMullen, and Amrit Yegneswaran

1. Hardware Interlock System Development

Mary Ann Antonioli, Peter Bonneau, Aaron Brown, and Tyler Lemon

- 1. Reviewed detector frame sensor location proposal from Carlos Munoz
 - It is *possible* to configure channels for 2-wire and 4-wire sensors in the same multiplexer investigating best method
- 2. Reviewed engineering drawing from Marc McMullen detailing DSG detector frame sensor location proposal
 - Will investigate whether there is an existing hole that can be used to route cables out of the detector frame
- 3. Reviewed tentative signal list; will add a location column
- 4. Interlock and trip delay code for hardware interlock LabVIEW program in progress
- 5. A spreadsheet detailing work plan for testing/assembly this summer will be generated
- 6. High voltage supply cable Radiall connector potting 14 of 40 complete
 - Potted cable testing will begin immediately

2. Ansys thermal analysis

Aaron Brown, Pablo Campero, and Brian Eng

- 1. Discussed electronics zone surface temperature calculations
- 2. Made modifications to streamline plot will increase linewidth

